

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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New A, 01/02/02

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Yuuichi HIRANO, et al.

FILING DATE

Herewith

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
VAMS	AO	3-156929	07/04/91	JAPAN (with English abstract)		X
VAMS	AP	6-130304	05/21/96	JAPAN (with English abstract)		X
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

VAMS	AW	K. IMAI, et al.; "A 0.13- μ m CMOS TECHNOLOGY INTEGRATING HIGH-SPEED AND LOW-POWER/HIGH-DENSITY DEVICES WITH TWO DIFFERENT WELL/CHANNEL STRUCTURES"; IEEE IEDM, 1999, pgs. 667-670.
	AX	
	AY	
	AZ	

Examiner

Victor A. Mandala g.

Date Considered

11-14-02

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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